

Doc code: IDS

Doc description: Information Disclosure Statement (IDS) Filed

PTO/SB/08a (03-09)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10001478
	Filing Date		2001-11-01
	First Named Inventor	Craig Nemecek	
	Art Unit	2123	
	Examiner Name	Jason Scott Proctor	
	Attorney Docket Number	CD01213M	

U.S.PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	6910126	B1	2005-06-21	Mar et al.	Entire Document
	2	6825689	B1	2004-11-30	Snyder	Entire Document
	3	6798299	B1	2004-09-28	Mar et al.	Entire Document
	4	7023257	B1	2006-04-04	Sullam	Entire Document
	5	6967511	B1	2005-11-22	Sullam	Entire Document
	6	6892322	B1	2005-05-10	Snyder	Entire Document
	7	6823282	B1	2004-11-23	Snyder	Entire Document
	8	7127630	B1	2006-10-24	Snyder	Entire Document

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Attorney Docket Number	CD01213M	

9	6611220	B1	2003-08-26	Snyder	Entire Document
10	7180342	B1	2007-02-20	Shutt et al.	Entire Document
11	6765407	B1	2004-07-20	Snyder	Entire Document
12	7386740	B1	2008-06-10	Kutz et al.	Entire Document
13	6952778	B1	2005-10-04	Snyder	Entire Document
14	7185162	B1	2007-02-27	Snyder	Entire Document
15	6854067	B1	2005-02-08	Kutz et al.	Entire Document
16	6614320	B1	2003-09-02	Sullam et al.	Entire Document
17	6868500	B1	2005-03-15	Kutz et al.	Entire Document
18	6859884	B1	2005-02-22	Sullam	Entire Document
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20	6981090	B1	2005-12-27	Kutz et al.	Entire Document
21	6603330	B1	2003-08-05	Snyder	Entire Document
22	6507214	B1	2003-01-14	Snyder	Entire Document
23	7005933	B1	2006-02-28	Shutt	Entire Document
24	6542025	B1	2003-04-01	Kutz et al.	Entire Document
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26	6957242	B1	2005-10-18	Snyder	Entire Document
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31	6563391	B1	2003-05-13	Mar	Entire Document
32	6608472	B1	2003-08-19	Kutz et al.	Entire Document
33	7185321	B1	2007-02-27	Roe et al.	Entire Document

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U.S. PATENT APPLICATION PUBLICATIONS

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NON-PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T ⁵
	1	USPTO Notice of Allowance for 10/238,966 (CD01250) dated 01/27/09; 4 pages	<input type="checkbox"/>

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2	USPTO Non-Final Rejection for 10/238,966 (CD01250) dated 06/30/08; 12 pages	<input type="checkbox"/>
3	USPTO Non-Final Rejection for 10/238,966 (CD01250) dated 12/26/07; 12 pages	<input type="checkbox"/>
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5	USPTO Non-Final Rejection for 10/238,966 (CD01250) dated 04/19/07; 7 pages	<input type="checkbox"/>
6	USPTO Non-Final Rejection for 10/238,966 (CD01250) dated 10/20/06; 8 pages	<input type="checkbox"/>
7	USPTO Non-Final Rejection for 10/238,966 (CD01250) dated 04/06/06; 8 pages	<input type="checkbox"/>
8	USPTO Notice of Allowance for 10/033,027 (CD00232) dated 03/31/09; 7 pages	<input type="checkbox"/>
9	USPTO Non-Final Rejection for 10/033,027 (CD00232) dated 12/18/08; 5 pages	<input type="checkbox"/>
10	USPTO Final Rejection for 10/033,027 (CD00232) dated 06/08/07; 8 pages	<input type="checkbox"/>
11	USPTO Non-Final Rejection for 10/033,027 (CD00232) dated 12/21/06; 31 pages	<input type="checkbox"/>
12	USPTO Final Rejection for 10/033,027 (CD00232) dated 08/09/06; 6 pages	<input type="checkbox"/>

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13	USPTO Non-Final Rejection for 10/033,027 (CD00232) dated 04/26/06; 26 pages	<input type="checkbox"/>
14	USPTO Final Rejection for 10/033,027 (CD00232) dated 10/31/05; 24 pages	<input type="checkbox"/>
15	USPTO Non-Final Rejection for 10/033,027 (CD00232) dated 04/20/05; 20 pages	<input type="checkbox"/>
16	USPTO Non-Final Rejection for 10/033,027 (CD00232) dated 10/18/04; 17 pages	<input type="checkbox"/>
17	Application Number 09/964,991 (CD00196): "A Novel Band-Gap Circuit for Providing an Accurate Reference Voltage Compensated for Process State, Process Variations and Temperature," Kutz et al.; 25 pages	<input type="checkbox"/>
18	Application Number 09/842,966 (CD00195): "Precision Crystal Oscillator Circuit Used in Microcontroller," Mar; 28 pages	<input type="checkbox"/>
19	"An Analog PPL-Based Clock and Data Recovery Circuit with High Input Jitter Tolerance," Sun, Reprinted from IEEE Journal of Solid-State Circuits, 1989; 4 pages	<input type="checkbox"/>
20	"WP 3.5: An Integrated Time Reference," Blauschild, Digest of Technical Papers, 1994; 4 pages	<input type="checkbox"/>
21	"Micropower CMOS Temperature Sensor with Digital Output," Bakker et al., IEEE Journal of Solid-State Circuits, 1996; 3 pages	<input type="checkbox"/>
22	USPTO Non-Final Rejection for 10/001,477 (CD01203) dated 05/11/05; 10 pages	<input type="checkbox"/>
23	USPTO Final Rejection for 10/001,477 (CD01203) dated 10/24/05; 13 pages	<input type="checkbox"/>

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24	USPTO Non-Final Rejection for 10/001,477 (CD01203) dated 03/02/06; 14 pages	<input type="checkbox"/>
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27	USPTO Notice of Allowance for 10/002,217 (CD01207) dated 10/14/08; 6 pages	<input type="checkbox"/>
28	USPTO Notice of Allowance for 10/002,217 (CD01207) dated 06/06/08; 7 pages	<input type="checkbox"/>
29	USPTO Final Rejection for 10/002,217 (CD01207) dated 02/06/08; 10 pages	<input type="checkbox"/>
30	USPTO Non-Final Rejection for 10/002,217 (CD01207) dated 08/03/07; 10 pages	<input type="checkbox"/>
31	USPTO Final Rejection for 10/002,217 (CD01207) dated 03/07/07; 12 pages	<input type="checkbox"/>
32	USPTO Non-Final Rejection for 10/002,217 (CD01207) dated 10/02/06; 21 pages	<input type="checkbox"/>
33	USPTO Non-Final Rejection for 10/002,217 (CD01207) dated 04/03/06; 12 pages	<input type="checkbox"/>
34	USPTO Final Rejection for 10/002,217 (CD01207) dated 11/17/05; 17 pages	<input type="checkbox"/>

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35	USPTO Non-Final Rejection for 10/002,217 (CD01207) dated 05/19/05; 15 pages	<input type="checkbox"/>
36	USPTO Notice of Allowance for 10/001,477 (CD01203) dated 11/10/08; 7 pages	<input type="checkbox"/>
37	USPTO Advisory Action for 10/001,477 (CD01203) dated 10/10/08; 3 pages	<input type="checkbox"/>
38	USPTO Final Rejection for 10/001,477 (CD01203) dated 06/30/08; 15 pages	<input type="checkbox"/>
39	USPTO Non-Final Rejection for 10/001,477 (CD01203) dated 12/06/07; 13 pages	<input type="checkbox"/>
40	USPTO Final Rejection for 10/001,477 (CD01203) dated 07/23/07; 16 pages	<input type="checkbox"/>
41	USPTO Non-Final Rejection for 10/001,477 (CD01203) dated 01/22/07; 15 pages	<input type="checkbox"/>

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	1	5982229	B1	1999-11-09	Wong et al.	Entire Document
	2	6535946	B1	2003-03-18	Bryant et al.	Entire Document
	3	4827401	B1	1989-05-02	Hrustich et al.	Entire Document
	4	5594734	B1	1997-01-14	Worsley et al.	Entire Document
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	7	6567426	B1	2003-05-20	van Hook et al.	Entire Document
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9	6249167	B1	2001-06-19	Oguchi et al.	Entire Document
10	4134073	B1	1979-01-01	MacGregor	Entire Document
11	5557762	B1	1996-09-17	Okuaki et al.	Entire Document
12	6771552	B1	2004-08-03	Fujisawa	Entire Document
13	5371860	B1	1994-12-06	Mura et al.	Entire Document
14	6575373	B1	2003-06-10	Nakano	Entire Document
15	5710906	B1	1998-01-20	Ghosh et al.	Entire Document
16	6294962	B1	2001-09-25	Mar	Entire Document
17	5590354	B1	1996-12-31	Klapproth et al.	Entire Document
18	5630102	B1	1997-05-13	Johnson et al.	Entire Document
19	5574892	B1	1996-11-12	Christensen	Entire Document

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20	5331571	B1	1994-07-19	Aronoff et al.	Entire Document
21	5552748	B1	1996-09-03	O'Shaughnessy	Entire Document
22	4689740	B1	1987-08-25	Moelands et al.	Entire Document
23	5559502	B1	1996-09-24	Schutte	Entire Document
24	5689196	B1	1997-11-18	Schutte	Entire Document
25	5670915	B1	1997-09-23	Cooper et al.	Entire Document
26	5684434	B1	1997-11-04	Mann et al.	Entire Document
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31	5428319	B1	1995-06-27	Marvin et al.	Entire Document
32	5604466	B1	1997-02-18	Dreps et al.	Entire Document
33	5898345	B1	1999-04-27	Namura et al.	Entire Document
34	5304955	B1	1994-04-19	Atriss et al.	Entire Document
35	5870345	B1	1999-02-09	Stecker	Entire Document
36	5870004	B1	1999-02-09	Lu	Entire Document
37	5699024	B1	1997-12-16	Manlove et al.	Entire Document
38	5345195	B1	1994-09-06	Cordoba et al.	Entire Document
39	4272760	B1	1981-06-09	Prazak et al.	Entire Document
40	4344067	B1	1982-08-10	Lee	Entire Document
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42	4947169	B1	1990-08-07	Smith et al.	Entire Document
43	5140197	B1	1992-08-18	Grider	Entire Document
44	5150079	B1	1992-09-22	Williams et al.	Entire Document
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46	5319370	B1	1994-06-07	Signore et al.	Entire Document
47	5440305	B1	1995-08-08	Signore et al.	Entire Document
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U.S.PATENT APPLICATION PUBLICATIONS

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	1	PCT/US99/09712	WO	A1	1999-02-25	Schutte	Entire Document	<input type="checkbox"/>
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33	Application Number 09/943,062 (CD00228): "Apparatus and Method for Programmable Power Management in a Programmable Analog Circuit Block;" Mar; 46 pages	<input type="checkbox"/>
34	Application Number 10/238,966 (CD01250M): "Method for Parameterizing a User Module;" Perrin; 41 pages	<input type="checkbox"/>
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47	USPTO Non-Final Rejection for 10/113,065 (CD01215M) dated 05/20/2005; 14 pages	<input type="checkbox"/>
48	Application Number 09/207,912 (CD98086): "Circuit(s), Architecture and Method(s) for Operating and/or Tuning a Ring Oscillator," Mar; 23 pages	<input type="checkbox"/>
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**INFORMATION DISCLOSURE
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First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	Jason Scott Proctor
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U.S. PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	6681280	B1	2004-01-20	Miyake, et al.	Entire Document
	2	5399922	B1	1995-03-21	Kiani et al.	Entire Document
	3	5680070	B1	1997-10-21	Anderson et al.	Entire Document
	4	6314530	B1	2001-11-06	Mann	Entire Document
	5	5438672	B1	1995-08-01	Dey	Entire Document
	6	6166367	B1	2000-12-26	Cho	Entire Document
	7	5600262	B1	1997-02-04	Kolze	Entire Document
	8	5414308	B1	1995-05-09	Lee et al.	Entire Document

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9	5258760	B1	1993-11-02	Moody et al.	Entire Document
10	5563526	B1	1996-10-08	Hastings et al.	Entire Document
11	6225866	B1	2001-05-01	Kubota et al.	Entire Document
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16	5703871	B1	1997-12-30	Pope et al.	Entire Document
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	2	0450863A2	EP	A1	1991-10-09	Austin	Entire Document	<input type="checkbox"/>
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	4	0308583A2	EP	A1	1989-03-29	Bartlett	Entire Document	<input type="checkbox"/>
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9	404083405	JP	A1	1992-03-17	Yasue	Entire Document	<input type="checkbox"/>
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	1	From Application Number 10/033,027 (CD00232): Goodenough, F. "Analog Counterparts of FPGAS Ease System Design" Electronic Design, Penton Publishing, Cleveland, OH, US vol. 42, no. 21, 10/14/94; 10 pages	<input type="checkbox"/>
	2	From Application Number 10/033,027 (CD00232): Harbaum, T. et al. "Design of a Flexible Coprocessor Unit" Proceedings of the Euromicro Conference, XX XX, Sept. 1999; 10 pages	<input type="checkbox"/>
	3	From Application Number 10/033,027; CD00169: "Programmable Microcontroller (PSoC) Architecture (Mixed Analog/Digital)"; 08/07/01; 09/924,734 Snyder et al.; 28 pages	<input type="checkbox"/>
	4	From Application Number 10/033,027; CD00170: "Digital Configurable Macro Architecture"; 07/18/01; 09/909,045; Snyder; 37 pages	<input type="checkbox"/>

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5	From Application Number 10/033,027: CD00172; "Configuring Digital Functions in a Digital Configurable Macro Architecture"; 07/18/01; 09/909,109; Snyder; 38 pages	<input type="checkbox"/>
6	From Application Number 10/033,027: CD00173; "A Programmable Analog System Architecture (As Amended)"; 07/18/01; 09/909,047; Mar; 60 pages	<input type="checkbox"/>
7	From Application Number 10/033,027: CD00174; "Programmable Methodology and Architecture for a Programmable Analog System (As Amended)"; 08/14/01; 09/930,021; Mar et al.; 87 pages	<input type="checkbox"/>
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20	GANAPATHY, GOPI, AND NARAYAN, RAM, AND JORDEN, GLEN, AND FERNANDEZ, DENZIL, AND WANG, MING, AND NISHIMURA, JIM; "Hardware Emulation for Functional Verification of K5", June 1996, 33rd Design Automation Conference Proceedings, 3-7 June 1996; 4 pages	<input type="checkbox"/>
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23	USPTO Advisory Action for 09/998,848 (CD01177) dated 09/07/05; 3 pages	<input type="checkbox"/>
24	Application Number 09/957,084 (CD00201): "A Crystal-Less Oscillator with Trimmable Analog Current Control for Increased Stability;" Mar; 28 pages	<input type="checkbox"/>
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12	WALTERS, STEPHEN; "Computer-Aided Prototyping for ASIC-Based Systems", 1991, IEEE Design & Test of Computers; Volume 8, Issue 2; 8 pages	<input type="checkbox"/>
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18	Wikipedia.org; "Von Neumann architecture"; retrieved from http://en.wikipedia.org/wiki/Von_Neumann_architecture on January 22, 2007; 4 pages	<input type="checkbox"/>
19	Stan Augarten; "The Chip Collection - Introduction - Smithsonian Institute"; "State of the Art"; "The First 256-Bit Static RAM"; retrieved on November 14, 2005 from http://smithsonianchips.si.edu/augarten/p24.htm ; 2 pages	<input type="checkbox"/>
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PTO/SB/08a (01-09)

Doc description: Information Disclosure Statement (IDS) Filed

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	10001478
Filing Date	2001-11-01
First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	Jason Scott Proctor
Attorney Docket Number	CD01213M

U.S.PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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	1	"Pod-Wikipedia, the free encyclopedia"; retrieved on November 14, 2005 from http://en.wikipedia.org/wiki/Pod ; 3 pages	<input type="checkbox"/>
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5	USPTO Non-Final Rejection for 09/989,767 (CD01178M) dated 07/24/08; 21 pages	<input type="checkbox"/>
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14	USPTO Final Rejection for 09/989,767 (CD01178M) dated 03/06/06; 15 pages	<input type="checkbox"/>
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18	CD00184; "Host to FPGA Interface in an In-Circuit Emulation System;" 10/10/01; 09/975,105; Nemecek; 44 pages	<input type="checkbox"/>
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27	USPTO Final Rejection for 09/989,778 (CD01172M) dated 02/05/07; 17 pages	<input type="checkbox"/>
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38	USPTO Final Rejection for 09/998,848 (CD01177M) dated 11/24/08; 15 pages	<input type="checkbox"/>
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1	USPTO Notice of Allowance for 09/992,076 (CD01210M) dated 11/13/2008; 15 pages	<input type="checkbox"/>
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15	"PSoC Technology Completely Changes 8-bit MCU System Design" Cypress MicroSystem, Inc. February 19, 2001; 21 pages	<input type="checkbox"/>
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Application Number	10001478
Filing Date	2001-11-01
First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	Jason Scott Proctor
Attorney Docket Number	CD01213M

18	USPTO Final Rejection for Application Number 09/943,062 (CD00228) dated 06/27/2003; 8 pages	<input type="checkbox"/>
19	USPTO Non-Final Rejection for Application Number 09/943,062 (CD00228) dated 01/27/2003; 7 pages	<input type="checkbox"/>
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22	Cypress Microsystems, "Cypress Microsystems Unveils Programmable System-on-a-Chip for Embedded Internet, Communications and Consumer Systems;" 2000, < http://www.cypressmicro.com/corporate/CY_Announces_nov_13_2000.html >; 3 pages	<input type="checkbox"/>
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27	USPTO Non-Final Rejection for Application Number 09/943,062 (CD00228) dated 06/22/2007; 12 pages	<input type="checkbox"/>
28	USPTO Miscellaneous Action for Application Number 09/943,062 (CD00228) dated 01/30/2006; 2 pages	<input type="checkbox"/>

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32	USPTO Non-Final Rejection for Application Number 10/113,064 (CD01200M) dated 04/25/2005; 15 pages	<input type="checkbox"/>
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Examiner Initial*	Cite No	Patent Number	Kind Code	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	6714066	B1	2004-03-30	Gorecki et al.	Entire Document
	2	6453175	B1	2002-09-17	Mizell et al.	Entire Document
	3	6185127	B1	2001-02-06	Myers et al.	Entire Document
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	6	5574678	B1	1996-11-12	Gorecki	Entire Document
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48	6061511	B1	2000-05-09	Martantz et al.	Entire Document
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50	6661288	B1	2003-12-09	Morgan et al.	Entire Document

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	1	20040153802	A1	2004-08-05	Kudo et al.	Entire Document
	2	20020059543	A1	2002-05-16	Cheng et al.	Entire Document
	3	20010010083	A1	2001-07-26	Satoh	Entire Document
	4	20020133794	A1	2002-09-19	Kanapathippillai et al.	Entire Document
	5	20020065646	A1	2002-05-30	Waldie et al.	Entire Document
	6	20020133771	A1	2002-09-19	Barnett	Entire Document

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Application Number	10001478
Filing Date	2001-11-01
First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	Jason Scott Proctor
Attorney Docket Number	CD01213M

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	1	USPTO Notice of Allowance for Application Number 09/875,599 (CD00180) dated 10/17/2006; 4 pages	<input type="checkbox"/>
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8	USPTO Final Rejection for Application Number 09/875,599 (CD00180) dated 08/25/2004; 17 pages	<input type="checkbox"/>
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	2	6373954	B1	2002-04-16	Malcolm et al.	Entire Document
	3	5900780	B1	1999-05-04	Hirose et al.	Entire Document
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	6	5248843	B1	1993-09-28	Billings	Entire Document
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10	5552725	B1	1996-09-03	Ray et al.	Entire Document
11	5455731	B1	1995-10-03	Parkinson	Entire Document
12	4970408	B1	1990-11-13	Hanke et al.	Entire Document
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14	6057705	B1	2000-05-02	Wojewoda et al.	Entire Document
15	6704879	B1	2004-03-09	Parrish	Entire Document
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17	5923264	B1	1999-07-13	Lavelle et al.	Entire Document
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20	5734272	B1	1998-03-31	Belot et al.	Entire Document
21	4999519	B1	1991-03-12	Kitsukawa et al.	Entire Document
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31	6507215	B1	2003-01-14	Piasecki et al.	Entire Document
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	1	20020121679	A1	2002-10-29	Bazarjani et al.	Entire Document
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	4	Hong et al., "Hierarchial System Test by an IEEE 1149.5 MTM-Bus Slave-Module Interface Core," IEEE, 2000; 14 pages	<input type="checkbox"/>
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8	Adham et al., "Preliminary Outline of the IEEE P1500 Scalable Architecture for Testing Embedded Cores," IEEE; 6 pages	<input type="checkbox"/>
9	Ghosh et al., "A Low Overhead Design for Testability and Test Generation Technique for Core-based Systems," IEEE, 1997; 10 pages	<input type="checkbox"/>
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41	Application Number 09/998,859 (CD01209M): "A System and a Method for Checking Lock Step Consistency between in Circuit Emulation and a Microcontroller while Debugging Process is in Progress;" Nemecek; 33 pages	<input type="checkbox"/>
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44	Application Number 09/989,574 (CD01164M): "Method and System for using a Graphics user Interface for Programming an Electronic Device;" Bartz et al.; 43 pages	<input type="checkbox"/>
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	2	5594388	B1	1997-01-14	O'Shaughnessy et al.	Entire Document
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Application Number	10001478
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First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	Jason Scott Proctor
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10	7250825	B1	2007-07-31	Wilson et al.	Entire Document
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	2	USPTO Non-Final Rejection for Application Number 11/850,260 (CD2007006) dated 03/06/2009; 7 pages	<input type="checkbox"/>
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	4	USPTO Advisory Action for Application Number 11/644,100 (CD05204) dated 02/09/2009; 3 pages	<input type="checkbox"/>
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13	Application Number 11/644,100 (CD05204): "Differential-to-single ended signal converter circuit and method," Stiff; 33 pages	<input type="checkbox"/>
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17	USPTO Non-Final Rejection for Application Number 11/132,894 (CD04064) dated 12/19/2006; 12 pages	<input type="checkbox"/>
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23	USPTO Final Rejection for Application Number 11/322,044 (CD05198) dated 09/21/2007; 14 pages	<input type="checkbox"/>
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37	USPTO Notice of Allowance for Application Number 10/871,582 (CD03036) dated 03/30/2006; 6 pages	<input type="checkbox"/>
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8	Durham et al., "Circuit Architectures for High Linearity Monolithic Continuous-Time Filtering," IEEE, 1992; 7 pages	<input type="checkbox"/>
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19	Application Number 09/747,262 (CD00085): "Linearized Digital Phase-Locked Loop," Williams et al.; 9 pages	<input type="checkbox"/>
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Art Unit	2123
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	1	Application Number 11/273,708 (CD05141): "Capacitance Sensor Using Relaxation Oscillators," Snyder et al.; 33 pages	<input type="checkbox"/>
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Application Number	10001478
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Art Unit	2123
Examiner Name	Jason Scott Proctor
Attorney Docket Number	CD01213M

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PTO/SB/08a (08-09)

Approved for use through 07/31/2009, OMB 0651-0031

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	10001478
Filing Date	2001-11-01
First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	Jason Scott Proctor
Attorney Docket Number	CD01213M

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